HIGH EFFICIENCY CODING/DECODING SYSTEM	
Patent Number:	JP6245201
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Requested Patent:	☐ <u>JP6245201</u>
Application Number:	JP19930024562 19930212
Priority Number(s):	
IPC Classification:	H04N7/133; H03M7/40; H04N5/92; H04N7/137
EC Classification:	
Equivalents:	
Abstract	
PURPOSE:To improve the transmission frequency of data, to facilitate the format conversion of data, and to attain decoding even when the data are reproduced discontinuously.  CONSTITUTION:A large block consists of four small blocks 50, a large block start code 110 and a large block header 52.  Each of the small blocks 50 has a variable length data 57, small block header 103 and an intra-data length 56, The data are sent through a branch 1, an MB ESCAPE 102 are sent through a branch 2, an MBSTUFF 59 are sent through a branch 3, and an SKIP 101 are sent through a branch 4. Even when the data are discontinuously reproduced, the data are decoded by the branches.	

Data supplied from the esp@cenet database - I2

25

【図15】H. 261の勧告案の圧縮法を説明するための説明図。

【図16】予測符号化を採用した記録再生装置の記録側 を示すブロック図。

【図17】従来例におけるマクロブロックの構成を示す 説明図。

【図18】 記録信号のデータストリームを示す説明図。

【図19】記録再生装置の復号側(再生側)を示すプロック図。

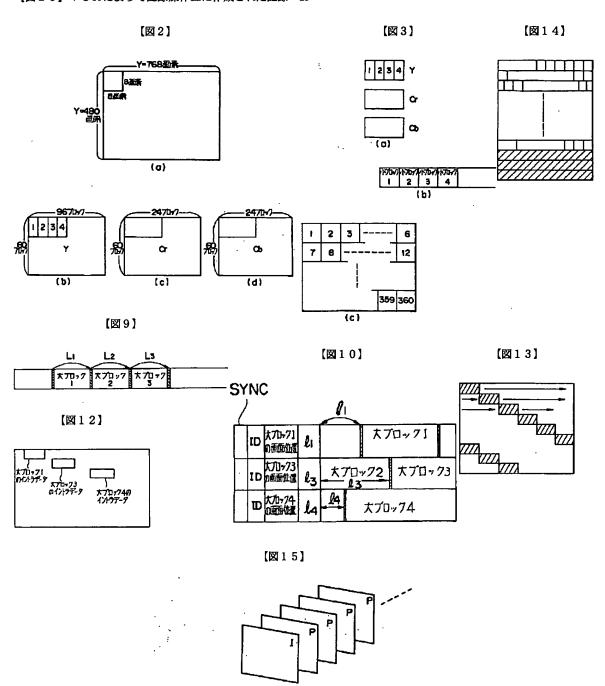
【図20】 VCRによって記録媒体上に作成された記録 10

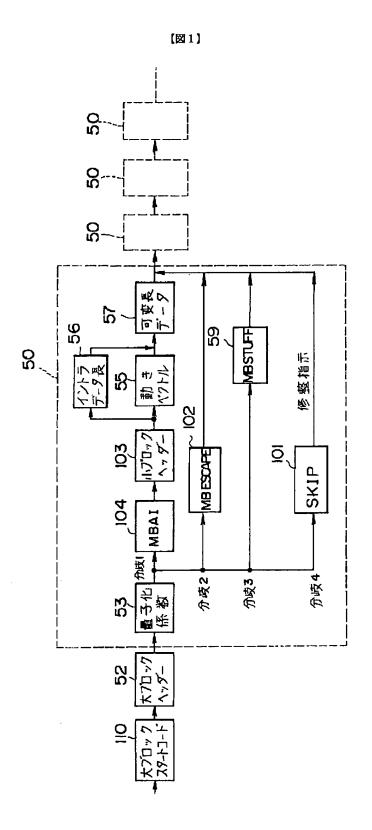
トラックを説明するための説明図。

【図21】MPEG方式の階層構造を説明するための説明図。

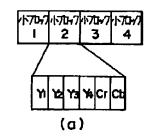
## 【符号の説明】

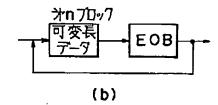
50…小プロック、52…大プロックヘッダー、56…イントラデータ長、57…可変長データ、59…MB STUF F、101 …SKIP、102 …MB ESCAPE、103 …小プロックヘッダー、110 …大プロックスタートコード

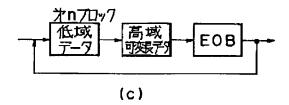


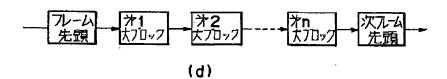


【図4】

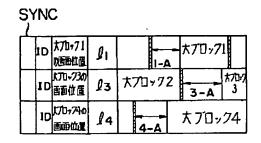




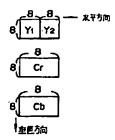




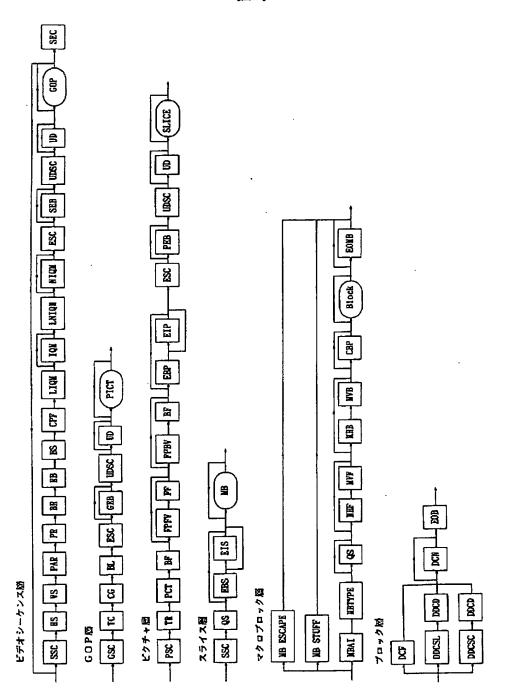
【図11】

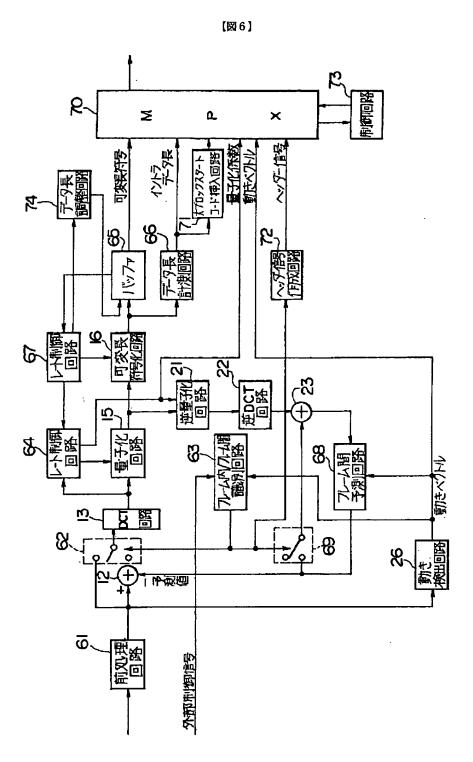


【図17】

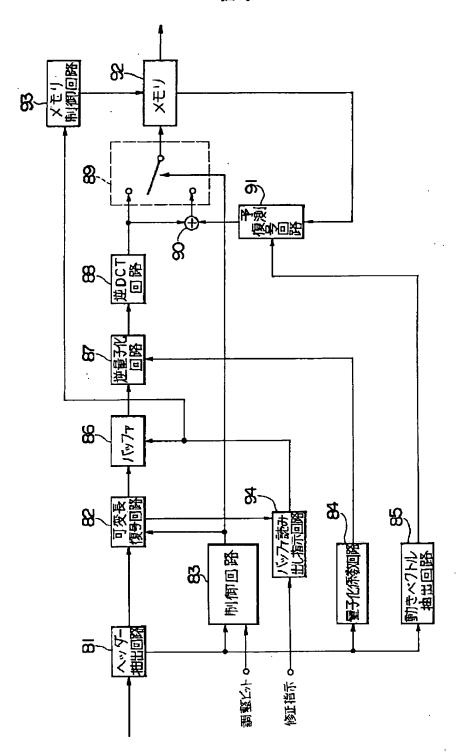


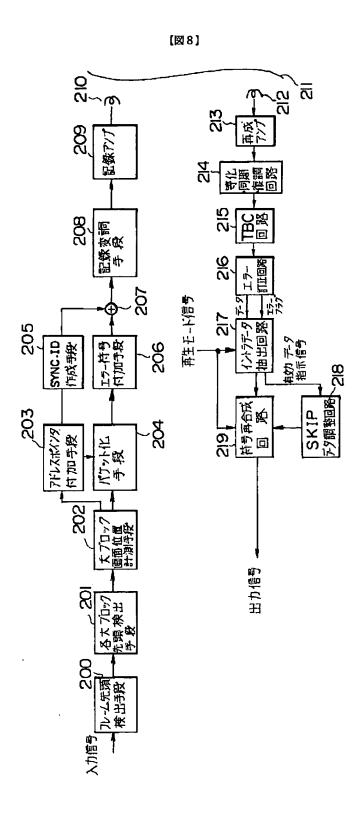
【図5】



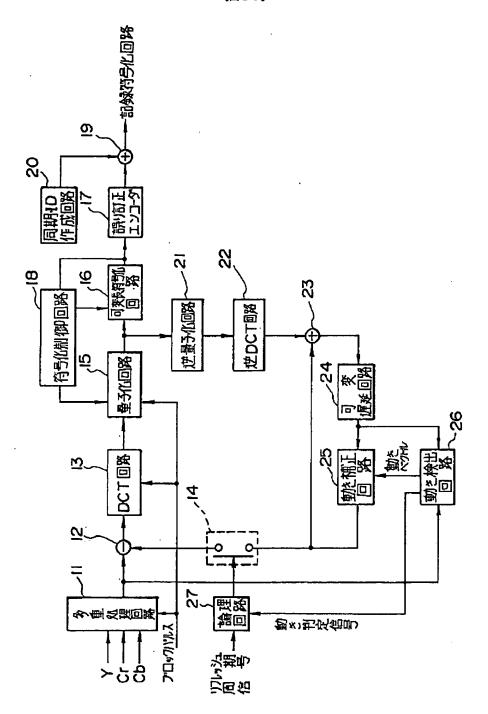


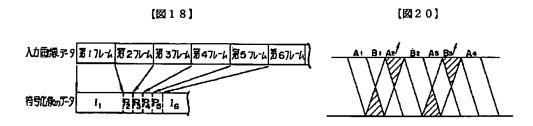
[図7]



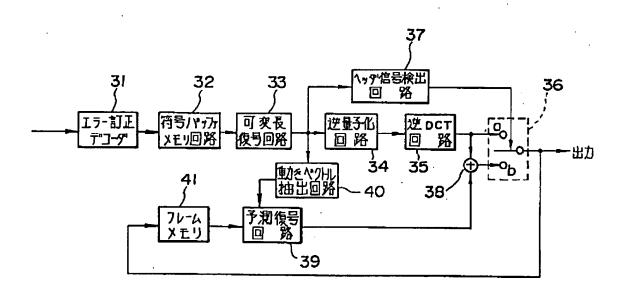


【図16】





【図19】



【図21】

